

Pat. No. 7,340,233

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yair Shemesh et al.

Docket: P19067

Patent No.: 7,340,233

Issued: March 4, 2008

Title: INTEGRATED CIRCUIT AND METHODS FOR THIRD SUB HARMONIC UP
CONVERSION AND DOWN CONVERSION OF SIGNALS

REVOCATION AND POWER OF ATTORNEY

CERTIFICATE UNDER 37 CFR § 3.73(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05 and 402.07, please
revoke any existing Powers of Attorney, if any, and appoint the following attorneys and/or patent
agents to prosecute this application and to transact all business in the Patent and Trademark
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Customer Number: 72517

CERTIFICATE UNDER 37 CFR § 3.73(b)

Intel Corporation hereby certifies that it is the assignee of the entire right, title and
interest in the patent application identified above by virtue of an assignment from the inventor
filed and 03/29/2004 and recorded on Reel 015158 Frames 0642 - 0645. To the best of my
knowledge and belief, title is in Intel Corporation, the assignee.

Pursuant to 37 C.F.R. § 3.73(b) I hereby declare that I, Pam J. Matlock, am empowered
to sign this certificate on behalf of Intel Corporation, the assignee.


I hereby declare that all statement made herein of my own knowledge are true, and that
all statements made on information and belief are believed to be true.

Please direct all correspondence in this case to:

Shiloh, et al.
C/o CPA Global
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Date April 22, 2009

By



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